COURSE DESCRIPTION CARD - SYLLABUS

Course name
FPGA-based system design [S2EiT1-MIEPU>PUwFPGA]

Course

Field of study
Electronics and Telecommunications

Year/Semester
2/3

Area of study (specialization)
Multimedia and Consumer Electronics

Profile of study
general academic

Level of study
second-cycle

Course offered in
Polish

Form of study
full-time

Requirements
elective

Number of hours

Lecture
15

Laboratory classes
30

Other (e.g. online)
0

Tutorials
0

Projects/seminars
0

Number of credit points

4,00

Coordinators

dr hab. inż. Olgierd Stankiewicz
olgierd.stankiewicz@put.poznan.pl

Lecturers

Prerequisites

Course objective

Course-related learning outcomes

Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows:
none

Programme content

Course topics

Teaching methods

Bibliography

Breakdown of average student's workload

	Hours	ECTS
Total workload	100	4,00
Classes requiring direct contact with the teacher	55	2,00
Student's own work (literature studies, preparation for laboratory classes/ tutorials, preparation for tests/exam, project preparation)	45	2,00